

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Stephen L. Morein et al.

Examiner: Dalip K. Singh

Serial No. 10/820,580

Art Group: 2628

Filing Date: April 8, 2004

Docket No.: 00100.02.0003

Confirmation No.: 8567

Title: **TWO LEVEL CACHE MEMORY ARCHITECTURE**

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT AND REMARKS

Dear Sir:

In response to the Final Office Action mailed October 24, 2006, for the above-identified patent application, Applicants respond as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 6 of this paper.